

Electronic Patent Application Fee Transmittal

Application Number:	10710699			
Filing Date:	29-Jul-2004			
Title of Invention:	ACCURATE TIMING ANALYSIS OF INTEGRATED CIRCUITS WHEN COMBINATORIAL LOGIC OFFERS A LOAD			
First Named Inventor:	Somasekar JAYARAMAN			
Filer:	William Brian Kempler/Shannon Trotter			
Attorney Docket Number:	TI-37107			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				
Extension - 3 months with \$0 paid	1253	1	1020	1020

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				1020